S.No.	Name of Student	Mentor	Date of Publication	Paper Title	Name and place of publication
1	Reibhu Sant	Prof Neelam Sharma	6-7 April 2018	Fast Addition Techniques using Hybrid Signed Digit Number System	International Conference on "Perspectives of MAKE IN INDIA (Trends, Route and Approach)" MITRA-2018, NIET, Alwar
2	Reibhu Sant	Abhilasha Gokhale	6-7 April 2018	Fast Addition Techniques using Hybrid Signed Digit Number System	International Conference on "Perspectives of MAKE IN INDIA (Trends, Route and Approach)" MITRA-2018, NIET, Alwar
3	Reibhu Sant	Prof Neelam Sharma	6-7 April 2018	Start-Up, Stand-Up Young Entrepreneurs: Vision and Action Plans	International Conference on "Perspectives of MAKE IN INDIA (Trends, Route and Approach)" MITRA-2018, NIET, Alwar
4	Reibhu Sant	Abhilasha Gokhale	6-7 April 2018	Start-Up, Stand-Up Young Entrepreneurs: Vision and Action Plans	International Conference on "Perspectives of MAKE IN INDIA (Trends, Route and Approach)" MITRA-2018, NIET, Alwar
5	Vansh Narula,	Rajni	6-7 April 2018	An Insight to Compressive Sampling	International Conference on "Perspectives of MAKE IN INDIA (Trends, Route and Approach)" MITRA-2018, NIET, Alwar

	Abhimanyu Chadha				
6	Vansh Narula, Abhimanyu Chadha	Sonia Jain	6-7 April 2018	An Insight to Compressive Sampling	International Conference on "Perspectives of MAKE IN INDIA (Trends, Route and Approach)" MITRA-2018, NIET, Alwar
	Rajat Sarkari	Prof Neelam Sharma	Feb. 2018	FPGA Synthesis of 32 bit MIPS basedpipelined RISC processor with UART interface	International journal in applid science & Engineering Technology, Volume 6, Issue 11, ISSN 2321-9653.
7	Reibhu Sant, Ashutosh Pal, Kunadan Jha	Prof Neelam Sharma	24th September, 2017	Design of Hybrid Signed Digit Adder for Fast Processing	International Conference on Science, Technology & Management (ICSTM-2017), IETE, Bengaluru

8	Reibhu Sant, Ashutosh Pal, Kunadan Jha	Abhilasha Gokhale	24th September, 2017	Design of Hybrid Signed Digit Adder for Fast Processing	International Conference on Science, Technology & Management (ICSTM-2017), IETE, Bengaluru
9	Akshat Malik	Prof Neelam Sharma	30 th April, 2017	Fast Adders: Timing, Layout and Cost Comparison	5th International Conference on Engineering and Technology, Science and Management Innovation (ICETSMI - 2017) , IETE, Lodhi Road, New Delhi
10	Akshat Malik	Abhilasha Gokhale	30 th April, 2017	Fast Adders: Timing, Layout and Cost Comparison	5th International Conference on Engineering and Technology, Science and Management Innovation (ICETSMI - 2017) , IETE, Lodhi Road, New Delhi
11	Reibhu Sant	Prof Neelam Sharma	10 th – 11 th March, 2017	Energy Crisis and the Next Step Forward	3rd International Conference on VLSI, Communication and Network (VCAN-2017), NIET, Alwar
12	Reibhu Sant	Abhilasha Gokhale	10 th – 11 th March, 2017	Energy Crisis and the Next Step Forward	3rd International Conference on VLSI, Communication and Network (VCAN-2017), NIET, Alwar

13	Akshat Malik	Prof Neelam Sharma	10 th – 11 th March, 2017	Performance Analysis of Signed Digit Adders	3rd International Conference on VLSI, Communication and Network (VCAN-2017), NIET, Alwar
14	Akshat Malik	Abhilasha Gokhale	10 th – 11 th March, 2017	Performance Analysis of Signed Digit Adders	3rd International Conference on VLSI, Communication and Network (VCAN-2017), NIET, Alwar
15	Akshat Malik	Prof Neelam Sharma	10 th – 11 th March, 2017	Performance Analysis of Signed Digit Adders	3rd International Conference on VLSI, Communication and Network (VCAN-2017), NIET, Alwar
16	Akshat Malik	Abhilasha Gokhale	1st – 3rd March, 2017	Fast Computing using Redundant Binary Signed Digit Adders	4th International Conference on Computing for Sustainable Global Development, BVICAM, Paschim Vihar, New Delhi
17	Akshat Malik	Prof Neelam Sharma	1st – 3rd March, 2017	Fast Computing using Redundant Binary Signed Digit Adders	4th International Conference on Computing for Sustainable Global Development, BVICAM, Paschim Vihar, New Delhi
18	Akshat Malik	Abhilasha Gokhale	1st – 3rd March, 2017	Fast Computing using Redundant Binary Signed Digit Adders	4th International Conference on Computing for Sustainable Global Development, BVICAM, Paschim Vihar, New Delhi

19	Himani Dhir, Shubham Jindal	Prof Neelam Sharma	Feb-17	Realization of Grounded Inductance and Capacitance using Current Feedback Operational Amplifier and its Application	International Journal of Electronics, Electrical and Computational System (IJEECS), ISSN 2348-117X, Volume 6, Issue 2, Page 96-101
20	Himani Dhir, Shubham Jindal	Praween Kumar Sinha	Feb-17	Realization of Grounded Inductance and Capacitance using Current Feedback Operational Amplifier and its Application	International Journal of Electronics, Electrical and Computational System (IJEECS), ISSN 2348-117X, Volume 6, Issue 2, Page 96-101
21	Abhinandan Agarwal, Gagandeep Singh	Prof Neelam Sharma	Apr-16	Implementation of AES Algorithm	International Journal of Engineering Research & Science (IJOER), ISSN (2395-6992), Vol-2, Issue-4, Page 112

22	Rohit Mishra	Prof Neelam Sharma	2015	A Configuration for Realizing Voltage Controlled Floating Inductance and Its Application	Circuit & Systems Scientific Research Publications (USA), Vol 6, PP 189-199
23	Rohit Mishra	Praween Kumar Sinha	2015	A Configuration for Realizing Voltage Controlled Floating Inductance and Its Application	Circuit & Systems Scientific Research Publications (USA), Vol 6, PP 189-199
24	Simran Agarwal, Sudipto Saha	Prof Neelam Sharma	Dec.2015	CFOA based Bandpass and Bandstop Ladder Filter – A New Configuration	Circuit & Systems, Scientific Research Publishing,
25	Simran Agarwal, Sudipto Saha	Praween Kumar Sinha	Dec.2015	CFOA based Bandpass and Bandstop Ladder Filter – A New Configuration	Circuit & Systems, Scientific Research Publishing,