LIST OF PUBLICATIONS ECE DEPARTMENT (Academic year: 2017-18)

Papers in Reviewed Journal

1. Reibhu Sant, Abhilasha Gokhale, Neelam Sharma, Ashutosh Pal, Kunadan Jha, "Design of Hybrid Signed Digit Adder for Fast Processing", International Journal of Electronics, Electrical and Computational System, Volume 6, Issue 9, September 2017, ISSN2348-117X.

2. Sonam Rewari, Vandana Nath, Subhasis Haldar, S.S.Deswal, and R.S. Gupta, "A Novel Design to Improve Band to Band Tunneling and to reduce Gate Induced Drain Leakages (GIDL) in Cylindrical Gate All Around (GAA) MOSFET", Microsystem Technologies Journal, DOI 10.1007/s00542-017-3446-1,Sept. 2017.

3. Sonam Rewari, Vandana Nath, Subhasis Haldar, S.S.Deswal, and R.S. Gupta, "Hafnium Oxide Based Cylindrical Junctionless Double Surrounding Gate (CJLDSG) MOSFET for High Speed, High Frequency Digital and Analog Applications," Microsystem Technologies Journal, DOI 10.1007/s00542-017-3436-3 Sept. 2017.

4. Manoj Kumar, Subhasis Haldar, Mridula Gupta, and R. S. Gupta, "Analytical model of threshold voltage degradation due to localized charges in gate material engineered schottky barrier cylindrical GAA MOSFET", Semiconductor Science and Technology, vol. 13, pp. 105013105023, July 2016. (Impact Factor : 2.098).

5. Manoj Kumar, Subhasis Haldar, Mridula Gupta, and R.S. Gupta, "Ambipolarity Reduction in DMG Asymmetric Vacuum Dielectric Schottky Barrier GAA MOSFET to Improve Hot Carrier Reliability", Superlattices and Microstructures, April 2017 (Accepted, In press). (Impact Factor : 2.117).

6. Manoj Kumar, Subhasis Haldar, Mridula Gupta, and R.S. Gupta, "Cylindrical Gate All Around Schottky Barrier MOSFET with Insulated Shallow Extensions at Source/Drain for Removal of Ambipolarity: A Novel Approach", Journal of Semiconductors, June 2017 (Accepted, In press).

7. NitinTrivedi, Manoj Kumar, Subhasis Haldar, S. S. Deswal, Mridula Gupta and R.S. Gupta, "Assessment of analog RF performance for insulated shallow extension (ISE) cylindrical surrounding gate (CSG) MOSFET incorporation gate stack", Microsystems and Technology, July, 2017. (Impact Factor : 0.974).

8. Yogesh Pratap, Manoj Kumar, Sneha Kabra, Subhasis Haldar, R.S. Gupta and Mridula Gupta, "Analytical modeling of Junctionless transistor for biomolecule detection", Journal of Computational Electronics, 2017. (Impact Factor : 1.104).

9. Nitin Trivedi, Manoj Kumar, Subhasis Haldar, S.S Deswal, Mridula Gupta and R. S. Gupta, " Charge plasma technique based dopingless accumulation mode junction less cylindrical surrounding gate MOSFET: analog performance improvement", Journal of Applied Physics A, Springer, Appl. Phys. A (2017) 123:564 DOI10.1007/s00339-017-1176-y.

10. “Gate Induced Drain Leakage (GIDL) Reduction in Cylindrical Dual Metal Hetero Dielectric (DM-HD) Gate All Around (GAA) MOSFET,” Sonam Rewari, Vandana Nath,Subhasis Haldar, S.S.Deswal and R.S.Gupta , IEEE Transactions on Electron Devices vol. 65, No. 1, January2018.

11. K. L. Pushkar, Ghanshyam Singh and R. K. Goel, “CMOS VDIBAs-based Singl

12. Resistance-Controlled Voltage-Mode Sinusoidal Oscillator,” Circuits and Systems, vol. 8,no. 1, 2017. http://dx.doi.org/10.4236/cs.2017.81002, ISSN 2153-1293.

13. K. L. Pushkar and D. R. Bhaskar “ New Single-Element-Controlled Sinusoidal Oscillator using Single VDIBA,” Journal of Engineering Technology, ISSN: 0747-9964, Vol. 4, Issue1.

14. K. L. Pushkar and K. Gupta, “MISO-type voltage-mode universal biquadratic filter using single Universal Voltage Conveyor,” Circuits and Systems, ID: 7601237, 2017., ISSN2153-1293.

15. K. L. Pushkar, “Electronically controllable sinusoidal oscillator employing DIBAs,”Advances in Electrical and Electronics Engineering, ISSN: 1804-3119, Vol. 15, Issue 2,2017. (Accepted)

16. N. Yadav, "DWT-SVD-WHT watermarking using varying strength factor derived from means of the WHT coefficients," Arabian Journal of Science and Engineering (SCIE indexed,Impact factor 0.865), 2017.

17. H. Khanna, M. Aggarwal and S. Ahuja, “Optimum Distance and Power allocation Strategies for Quantum-limited Inter-Relayed FSO Communication System,”AEU–International Journal of Electronics and Communications, DOI –10.1016/ j.aeue.2017.06.015, vol. 80, pp. 10-18, 2017.

18. H. Khanna, M. Aggarwal and S. Ahuja, “On the end-to- end performance of a mixed RF-FSO link with a Decode and Forward relay, ”JournalofOptical Communications,DOI10.1515/joc-2017-0077, ISSN (Online) 2191-6322, ISSN (Print) 0173-4911, Jul. 2017.

19. “Fast Adders: Timing, Layout and Cost Comparison”, Akshat Malik, Abhilasha Gokhale and Neelam Sharma, International Journal of Electronics, Electrical and Computational System (IJEECS) ISSN 2348-117X, Volume 6, May 2017.

20. Divya Goyal and Pragya Varshney. "CCII and RC fractance based fractional ordercurrentintegrator." MicroelectronicsJournal 65(2017):10.DOI10.1016/j.mejo.2017.05.002

21. O.P.Verma, Nitin sharma, “Intensity preserving cast removal in color images using particl e swarm optimization”, Journal of Electrical and Computer Engineering, ISSN 20888708, Vol.7 No.5,2017.

22. O.P.Verma, Nitin sharma, Efficient Color Cast Correction Based on Fuzzy Logic. In Journal of Engineering Science and Technology Review, Vol. 10 issue 3, 2017.

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24. Sonam Rewari, Vandana Nath, Subhasis Haldar, S.S.Deswal, and R.S. Gupta, "A Novel Design to Improve Band to Band Tunneling and to reduce Gate Induced Drain Leakages (GIDL) in Cylindrical Gate All Around (GAA) MOSFET", Microsystem Technologies Journal, DOI 10.1007/s00542-017-3446-1, Sept. 2017.

25. Sonam Rewari, Vandana Nath, Subhasis Haldar, S.S.Deswal, and R.S. Gupta, "Hafnium Oxide Based Cylindrical Junctionless Double Surrounding Gate (CJLDSG) MOSFET for High Speed, High Frequency Digital and Analog Applications," Microsystem Technologies Journal, DOI 10.1007/s00542-017-3436-3 Sept. 2017.

26. "Gate Induced Drain Leakage (GIDL) Reduction in Cylindrical Dual Metal Hetero Dielectric (DM-HD) Gate All Around (GAA) MOSFET," Sonam Rewari, Vandana Nath, Subhasis Haldar, S.S.Deswal and R.S.Gupta , IEEE Transactions on Electron Devices vol. 65, No. 1, January2018.

27. Hemant Tulsani, Palak Chawla, Rashmi Gupta, "A Novel Steganographic Model for Securing Binary Images", International Journal of Information Technology, Springer, 2017

Papers in International Conferences

1. Reibhu Sant, Abhilasha Gokhale, Neelam Sharma, Ashutosh Pal, Kunadan Jha, "Design of Hybrid Signed Digit Adder for Fast Processing", International Conference on Science, Technology & Management (ICSTM-2017), 24th September, 2017, IETE, Bengaluru

2. "Carrier concentration dependence f ballistic mobility and mean free path in a nano-dimensional InAlAs/InGaAs Single Gate HEMT" Neetika Sharma, Jyotika Jogi, R. S. Gupta, UPCON-2016, 09-11 December, IIT BHU, Uttar Pradesh, India.

3. "Theoretical Investigations of Junctionless Nan wire Transistor for High Reliability and Digital Applications" Nitin Trivedi, Manoj Kumar, Subhasis Haldar, S.S Deswal, Mridula Gupta and R. S. Gupta, UPCON-2016, 09-11 December, IIT BHU, Uttar Pradesh, India.

4. Nitin Trivedi, Manoj Kumar, Subhasis Haldar, S.S Deswal, Mridula Gupta and R. S. Gupta, " Effect of Temperature on the Performance Analysis of Junctionless Accumulation Mode (JAM) Surrounding Gate MOSFET " IWPSD, IIT Delhi, 11-15 Dec., 2017.

5. Nisha Chugh, Monika Bhattacharya, Manoj Kumar and R. S. Gupta, " Impact of Temperature and Al composition on the threshold voltage and sheet carrier concentration of AlmGa1-mN/GaN/ AlmGa1-mN Double Heterostructure HEMT" in IWPSD, IIT Delhi - Accepted and to be presented in Dec, 2017.

6. Nisha Chugh, Monika Bhattacharya, Manoj Kumar and R. S. Gupta "Sheet Carrier Concentration and Threshold Voltage Modeling of Asymmetrically Doped AlGaN/GaN/AlGaN Double Heterostructure HEMT" in UPCON (IEEE Conference), held at GLA University, Mathura, Oct, 2017.

7. Manjula Vijh, R.S. Gupta, Sujata Pandey, "GaN Based Tunnel Field Effect Transistor for Terahertz Applications" , PIERS 2017 in Singapore (Presented)

8. Manjula Vijh, R.S. Gupta, Sujata Pandey, "Switching Characteristics of InN Tunnel Field Effect Transistor and Its Application in the Design of RF Amplifiers", PIERS 2017 in Singapore (Presented)

9. Sunil Mathur, Tapan Sharma, Dr. Vinod Shokeen "Best of Breed Solution for Clustering of Satellite Images Using Big data Platform Spark" International Conference on Inventive Communication and Computational Technologies (ICICCT 2017)

10. Dinesh Prasad, Javed Ahmad and Mayank Srivastava , " New CM/VM 3 rd Order Quadrature oscillator Using VDCCs.," 2017 IEEE International Conference on Applied System Innovation (IEEE ICASI 2017) May 13-17,2017, Sapporo , Japan .

11. Reibhu Sant, Abhilasha Gokhale, Neelam Sharma, Ashutosh Pal, Kunadan Jha, "Design of Hybrid Signed Digit Adder for Fast Processing", International Conference on Science, Technology & Management (ICSTM-2017), 24th September, 2017, IETE, Bengaluru.

12. Rajni and C.S.Rai, "Linear Phase Constrained Affine Projection Sign Algorithm" in International Conference on Electrical, Electronics, Computers, Communication, Mechanical and Computing (EECCMC), January,2018.

13. Rajni and C.S.Rai, "PNLMS-Based Computational Cost Efficient Adaptive Algorithms for Sparse Channel Identification", in International Conference on Electrical,Electronics,Computers, Communication, Mechanical and Computing (EECCMC), January,2018.

14. Hemant Tulsani, Palak Chawla, Rashmi Gupta, ;Visual Cryptography and Integer Wavelet Transform based Watermarking for Securing Binary Images & quot;, IEEE International Conference on Computing for Sustainable Global Development, Mar. 2017.

15. Hemant Tulsani, Palak Chawla, Rashmi Gupta, ;Visual Cryptography and Integer Wavelet Transform based Watermarking for Securing Binary Images & quot;, IEEE International Conference on Computing for Sustainable Global Development, Mar. 2017.

16. Vaibhav, Davinder Miglani, Shivam Gupta, Karan Arora; Design of Low Power High Speed Current Comparator based flash ADCs ;IEEE International Conference on Computing for Sustainable Global Development, Mar. 2018.(Accepted for publication).