S.No	Name Of The Faculty	Number of quality publications in refereed/SCI Journals,Conferences Books/Book Chapters etc.
1.	Prof (Dr) Neelam Sharma	3
2.	Prof. R.S.Gupta	28
3.	Prof. Sunil Kumar	5
4.	Mr. R. K. Choudhary	4
5.	Dr.Parween Kr Sinha	4
6.	Dr.Himanshu Khanna.	4
7.	Mr.Amit Saxena	3
8.	Dr.Navneet Yadav.	9
9.	Ms.PreetiGoyal	3
10.	Ms.Anubha Goel	15
11.	Mr.Umesh Ch. Singh.	4
12.	Dr.Nitin Sharma	2
13.	Mr.Sumanta Kr. Kundu	13
14.	Mrs. Swati Sharma	3
15.	Mrs.Rajni	14
16.	Ms. Sonia Jain	2
17.	Mrs.SonamRewari	13
18.	Ms.Kanika Aggarwal	2
19.	Ms.Sumedha Gupta	4
20.	Mr.Vaibhav Nijhawan	4
21.	Mrs.Neeraj Malik	4
22.	Mrs.ShaluGarg	2
23.	Mr.Sudarshan Kumar	2
24.	Mrs.Anamika Jain	3
25.	Mr.RohitRana	4
26.	Mr. Ajay K Gupta	1

1. Praween Kumar Sinha, and **Neelam Sharma**, "Optimized Two Stage Low Power Miller Compensated Operational Amplifier with CMOS 180nm Technology" Journal of Active & Passive Electronic Devices 15 (2020).
2. Praween Kumar Sinha, Mohit Kumar, Gautam Kunal Haruray, and **Neelam Sharma.** "CFOA based Integrator Suitable for Analog Signal Processing" COJ Electronics & Communications, 2018.
3. Praween Kumar Sinha, Mudit Garg, and **Neelam Sharma**, "Ladder Based High Pass Filter Using VDTA", Journal of Active & Passive Electronic Devices, pp. 1–8, 2021
4. Anubha Goel, SonamRewari, Seema Verma and **R.S. Gupta**, "Shallow Extension Engineered Dual Material Surrounding Gate (SEE-DM-SG) MOSFET for Improved Gate Leakages, Analysis of Circuit and Noise Performance," AEU-International Journal of Electronics and Communications (Elsevier), Volume: 111, pp 152924, Nov 2019. (Print ISSN: Online ISSN: 1434-8411 Digital Object Identifier: 10.1016/j.aeue.2019.152924, Impact Factor: 2.853).
5. Anubha Goel, Sonam Rewari, Seema Verma and **R. S. Gupta**, " High-K Spacer Dual-Metal Gate Stack UnderlapJunctionless Gate All Around (HK-DMGS-JGAA) MOSFET for High Frequency Applications, " Microsystem Technologies Journal (Springer),

Volume: 26, Issue: 5, pp 1697-1705, Dec 2019. (Print ISSN: 0946-7076 Online ISSN: 1432-1858; Digital Object Identifier: 10.1007/s00542-019-04715-6, Impact Factor: 1.513).

6. Anubha Goel, Sonam Rewari, Seema Verma and **R.S. Gupta**, "Temperature-dependent gate-induced drain leakages assessment of dual-metal nanowire field-effect transistor—analytical model" IEEE Transactions on Electron Devices, Volume: 66, Issue: 5 ,pp 2437 – 2445, May 2019. (Print ISSN: 0018-9383 Online ISSN: 1557-9646 Digital Object Identifier 10.1109/TED.2019.2898444, Impact Factor: 2.62).
7. Anubha Goel, Sonam Rewari, Seema Verma and **R. S. Gupta**, "Modeling of Shallow Extension Engineered-Dual Metal-Surrounding Gate (SEE-DM-SG) MOSFET- Gate Induced Drain Leakages (GIDL)," Indian Journal of Physics (Springer), Volume: 95, Issue: 2, pp 299-308, March 2020. (Online ISSN: 0946-7076, Digital Object Identifier: 10.1007/s12648-020-01704-8, Impact Factor: 1.242).
8. Anubha Goel, Sonam Rewari, Seema Verma and **R. S. Gupta**, "Physics based Analytic Modeling and Simulation of Gate-Induced-Drain-Leakage and Linearity Assessment in Dual-Metal Junctionless Accumulation Nano-Tube FET (DM-JAM-TFET)," Applied Physics A (Springer), Volume:126, pp. 1-4, May 2020. (Print ISSN: 0947-8396, Online ISSN: 1432-0630, Digital Object Identifier: 10.1007/s00339-020-03520-7, Impact Factor: 1.784).
9. Anubha Goel, Sonam Rewari, Seema Verma and **R. S. Gupta**, "Novel Dual-Metal Junctionless Nanotube FET (DMJN-TFET) for Improved Analog and Low Noise Applications," Journal of Electronic Materials (Springer), Volume: 50, Issue: 1, pp.: 108-119, Nov. 2020. (Print ISSN: 0361-5235, Digital Object Identifier: 10.1007/s11664-020-08541-9, Impact Factor: 1.774).
10. Anubha Goel, Sonam Rewari, Seema Verma and **R. S. Gupta** "Shallow Extension Engineered Dual Metal Surrounding Gate (SEE-DM-SG) MOSFET for lower leakages and Submillimeter Wave Applications", , 12th INDIACom; 2018 5th IEEE International Conference on Computing for Sustainable Global Development, BVICAM, 14-16 March, 2018, New Delhi, India.(ISSN: 0973-7529 ISBN: 978-93-80544-28-1)
11. Anubha Goel, Sonam Rewari, Seema Verma and **R. S. Gupta**, "Impact of Shallow Extension Engineering (SEE) on Analog and Digital Performance of Surrounding Gate (SG) MOSFET for High Frequency, High Gain and High Speed Real Time Applications", Micro2018; 5th International Conference on Microelectronics, Circuits & Systems, Applied Computer Technology, May, 19th - 20th , 2018, Bhubaneswar, Odisha, India.(ISBN: 81-85824-46-1)
12. Anubha Goel, Sonam Rewari, Seema Verma and **R. S. Gupta**, "Dielectric Modulated Triple Metal Gate All Around MOSFET (TMGAA) for DNA Bio-Molecule Detection", 2018 IEEE Electron Device Kolkata Conference (2018 IEEE EDKCON), The Pride Hotel, Kolkata, 24-25 November, 2018, Organized by IEEE EDS Kolkata Chapter, EDKCON 2018, Kolkata, India.(ISBN: 978-1-5386-6415-5)
13. Anubha Goel, SonamRewari, Seema Verma and **R. S. Gupta**, "GaN Based Dual-Metal Gate Stack Engineered Junctionless-Surrounding-Gate (DMSEJSG) MOSFET for High Power Applications", 16th IEEE India Council International Conference; Indicon-2019 on "Applying Artificial Intelligence in Engineering for Prosperity and Betterment of Humanity", 13-15 Dec, 2019 in Marwadi University, Rajkot, Gujarat, India. (ISBN: 978-1-7281-2327-1)

14. Anubha Goel, Sonam Rewari, Seema Verma and **R. S. Gupta** "Dual-Metal Junctionless Nanotube FET (DMJN-FET) for Improved Analog Applications", AnubhaGoel, SonamRewari, SeemaVerma and R.S.Gupta, 8th International conference ; CCSN-2019 on "Computing, Communication and Sensor Networks", 19-20 Oct, 2019 in Institute of Aeronautical Engineering, Hyderabad, Telengana, India.(ISBN: 81-85824-5)
15. Anubha Goel, Sonam Rewari, Seema Verma and **R. S. Gupta**, "High-K Spacer Dual-Metal Gate Stack UnderlapJunctionless Gate All Around (HK-DMGSU-JGAA) MOSFET for High Frequency Applications", 8th International conference ; CCSN-2019 on "Computing, Communication and Sensor Networks", 19-20 Oct, 2019 in Institute of Aeronautical Engineering, Hyderabad, Telengana, India. (ISBN:81-85824-5)
16. Amit Saxena, Manoj Kumar, R. K. Sharma, and **R. S. Gupta**. "SOI Schottky barrier nanowire MOSFET with reduced ambipolarity and enhanced electrostatic integrity." Journal of Electronic Materials 49 (2020): 4450-4456
17. Amit Saxena, Manoj Kumar, R. K. Sharma, and **R. S. Gupta**, "SOI-Schottky Barrier Nanowire MOSFET for Reduced Ambipolarity and Enhanced Electrostatic Integrity", 6th Microelectronics , Circuits and Systems (Micro 2019), Kolkata, 6th-7th July, West Bengal, India, 2019.
18. Preeti Goyal, Garima Srivastava, Sonam Rewari, and **R. S. Gupta**, "Controlling Ambipolarity and Rising Ion in TFETs for Enhanced Reliability: A Review", 5th IEEE International Conference on Recent Advances and Innovations in Engineering- ICRAIE 2020.
19. Swati Sharma,Sonam Rewari, Vandana Nath, S. S. Deswal, **R. S. Gupta,** "Schottky Barrier Double Surrounding Gate MOSFET For High-Frequency Implementation" 5th IEEE International Conference on Recent Advances and Innovations in Engineering-ICRAIE 2020 December1-3.
20. Sumedha Gupta, Neeta Pandey and **R. S. Gupta**, "Investigation of Dual-Material Double Gate Junctionless Accumulation-Mode Cylindrical Gate All Around (DMDG-JLAM-CGAA) MOSFET with High-k Gate Stack for low Power Digital Applications" *2020 IEEE 17th India Council International Conference (INDICON)*, New Delhi, India, 2020, pp. 1-4, doi: 10.1109/INDICON49873.2020.9342380.
21. Sumedha Gupta, Neeta Pandey and **R. S. Gupta**. Analog Performance of Dual-Metal Gate Stack Architecture of Junctionless Accumulation-Mode Cylindrical Surrounding Gate (DMGSA-JAM-CSG) MOSFET. In2021 Devices for Integrated Circuit (DevIC) 2021 May 19 (pp. 1-5). IEEE.
22. Sumedha Gupta, Neeta Pandey and **R. S. Gupta**. Analytical modeling of dual-metal gate stack engineered junctionless accumulation-mode cylindrical surrounding gate (DMGSE-JAM-CSG) MOSFET. Applied Physics A. 2021 Jul;127(7):1-0.
23. Neeraj, Sharma S, Goel A, Rewari S, **R. S. Gupta**. Gate-Stack Dual Metal (DM) Nanowire FET with Enhanced Analog Performance for High Frequency Applications. In 2021 Devices for Integrated Circuit (DevIC) 2021 May 19 (pp. 373-377). IEEE
24. Neeraj,Goel A, Sharma S, Rewari S, **R. S. Gupta**. SiC-based analytical model for gate-stack dual metal nanowire FET with enhanced analog performance. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields.:e2986, Jan, 2022.

25. Swati Sharma, Anubha Goel, Sonam Rewari, Vandana Nath and **R. S. Gupta** "Enhanced Analog Performance and High-Frequency Applications of Dielectric Engineered High-K Schottky Nanowire FET," Silicon-Springer.Jan 2022
26. Sumedha Gupta, Neeta Pandey and **R. S. Gupta**. Temperature dependency and linearity assessment of dual-metal gate stack junctionless accumulation-mode cylindrical surrounding gate (DMGS-JAM-CSG) MOSFET. PhysicaScripta. 2021 Nov 8;96(12):124055.
27. Preeti Goyal, Garima Srivastava, Sonam Rewari, and **R. S. Gupta**. Performance Analysis of Drain Pocket Hetero Gate Dielectric DG-TFET: Solution for Ambipolar Conduction and Enhanced Drive Current. Silicon. 2022 Jan 7:1-1.
28. Amit Saxena, Manoj Kumar, R. K. Sharma, and **R. S. Gupta**. Design of First Order Active Low Pass Filter using 22nm Gate All Around Silicon-on-Insulator Schottky Barrier MOSFET. December, 2021.
29. Amit Saxena, Manoj Kumar, R. K. Sharma, and **R. S. Gupta**. Non-Quasi-Static Model Parameter Extraction for Cylindrical Silicon-on-Insulator Schottky Barrier MOSFET. December, 2021.
30. Preeti Goyal, Srivastava G, Madan J, Pandey R, **Gupta RS**. Source Material- Engineered Charge Plasma based Double Gate TFET for Analog/RF Applications In2021 IEEE International Conference on Industrial Electronic Research and Application (ICIERA) 2021 Dec 24 . IEEE.
31. Swati Sharma, Anubha Goel, Sonam Rewari, Vandana Nath and **R. S. Gupta** .Gallium Nitride Cylindrical Schottky Barrier MOSFET(GaN-CSB-MOSFET) For High - Frequency Implementation. IEEE International Conference on Industrial Electronics Research and Application (ICIERA 2021), Dec, 2021.
32. Tapan Sharma, Vinod Shokeen, and **Sunil Mathur** "Distributed Approach to Process Satellite Image Edge Detection on Hadoop Using Artificial Bee Colony." International Journal of Service Science, Management, Engineering, and Technology (IJSSMET) 11, no. 2 (2020): 80-94.
33. Vaibhav Mathur, Divyansh Jha, **Sunil Kumar**, "Self-Attention Based Visual Dialogue", International Journal of Recent Technology and Engineering (IJRTE), ISSN: 2277-3878, Volume-8 Issue-3, September 2019
34. Jeebananda Panda, Divant Jain, Lavi Tanwar, and **Sunil Kumar** "Watermarking of Multi-mode Counter Circuit using Hardware Efficient Algorithm." Educational Research (IJMCER) 2, no. 5 (2020): 82-95.
35. Vaibhav Mathur, Anshu Mathur, and **Sunil Kumar**. "A Comparative Study of Soft Computing Paradigms for Automatic Humour Detection in Tweets" In 2019 6th International Conference on Computing for Sustainable Global Development (INDIACom), pp. 975-979. IEEE, 2019.
36. Tapan Sharma, Vinod Shokeen, and **Sunil Mathur** "Comparison of approaches of distributed satellite image edge detection on Hadoop" In 2018 Second International Conference on Inventive Communication and Computational Technologies (ICICCT), pp. 645-649. IEEE, 2018.
37. Sampurnananda Mishra, ChandraKanta Samal, Navneet Yadav, **Rama Kanta Choudhury**, "The Impact of Various Digitized Social Networking Media through Text,

Images and Videos on Language Usage", International Journal of Scientific & Technology Research, 2019. (SCOPUS Indexed).
38. Navneet Yadav, **R. K. Choudhury**, "Connected Vehicles: Intelligent Transport Systems", Smart Sensors for Industrial Internet of Things. Internet of Things (Technology, Communications and Computing). Springer, Cham, 2019. https://doi.org/10.1007/978-3-030-52624-5_6
39. **R.K. Choudhury**, Navneet Yadav, Jaideep Kala, Sonalika Bhandari, "Real Time Monitoring And Analysis of Troposphere Pollutants Using A Multipurpose Surveillance Drone" Internet of Drones: Opportunities and Challenges, Apple Academic Press, 2021
40. A. Bawa, **R. K. Choudhury**, C. K. Samal, Navneet Yadav, "Programmable Joint Computing Filter for Low-Power and High-Performance Applications" Advanced Computing. IACC 2020. Communications in Computer and Information Science, vol 1368. Springer, Singapore, 2021. https://doi.org/10.1007/978-981-16-0404-1_22
41. **Praween Kumar Sinha**, and Neelam Sharma, "Optimized Two Stage Low Power Miller Compensated Operational Amplifier with CMOS 180nm Technology" Journal of Active & Passive Electronic Devices 15 (2020).
42. **Praween Kumar Sinha**, Mohit Kumar, Gautam Kunal Haruray, and Neelam Sharma. "CFOA based Integrator Suitable for Analog Signal Processing" COJ Electronics & Communications, 2018.
43. **Praween Kumar Sinha**, SuyashAhluwalia, and Deepanshu Gupta, "8 BIT Single cycle processor" Technology 10, no. 2 (2019): 160-168.
44. **Praween Kumar Sinha**, MuditGarg, and Neelam Sharma, "Ladder Based High Pass Filter Using VDTA", Journal of Active & Passive Electronic Devices, pp. 1–8, 2021
45. **HimanshuKhanna**, Mona Aggarwal, and SwaranAhuja, "A novel project-and-forward relay-assisted mixed RF-FSO system design and its performance evaluation" Transactions on Emerging Telecommunications Technologies 30.5 (2019): e3584.
46. **Himanshu Khanna** "Symbol error probability evaluation for PSK modulation schemes in generalized non-Gaussian noise channels" International Journal of Communication Systems 33.12 (2020): e4450.
47. **Himanshu Khanna** "Performance evaluation of AF-relayed mixed FSO/mm-wave-RF link modeled by generalized misalignment and RF fading distributions" Frequenz 75.1-2 (2021): 9-18.
48. **Himanshu Khanna**. On the unified error performance of PSK modulation schemes over mm-wave wireless channels affected by carrier phase synchronization errors. International Journal of Communication Systems. 2021 May 10;34(7):e4712
49. **Amit Saxena**, Manoj Kumar, R. K. Sharma, and R. S. Gupta. "SOI Schottky barrier nanowire MOSFET with reduced ambipolarity and enhanced electrostatic integrity." Journal of Electronic Materials 49 (2020): 4450-4456.
50. **Amit Saxena**, Kumar M, Sharma RK and Gupta RS. Design of First Order Active Low Pass Filter using 22nm Gate All Around Silicon-on-Insulator Schottky Barrier MOSFET. December, 2021.
51. **Amit Saxena**, Kumar M, Sharma RK and Gupta RS. Non-Quasi-Static Model Parameter Extraction for Cylindrical Silicon-on-Insulator Schottky Barrier MOSFET. December, 2021.

52. Sudan Jha, Raghvendra Kumar, Le Hoang Son, Jyotir Moy Chatterjee, ManjuKhari, **NavneetYadav**, FlorentinSmarandache, "Neutrosophic soft set decision making for stock trending analysis", Evolving Systems, 2018, DOI 10.1007/s12530-018-9247-7 (ESCI - Emerging Sources Citation Index).
53. Samarth Negi, **NavneetYadav**, Rahul Rawat, Rishabh Singh, "An effective technique for determining fish freshness using image processing", International Journal of Innovative Technology and Exploring Engineering (IJITEE), Vol. 8, Issue 9S, pp. 460-464, 2019. DOI: 10.35940/ijitee.I1073.0789S19, (SCOPUS Indexed).
54. **NavneetYadav**, NavdeepGoel, "An effective image adaptive watermarking scheme with transform coefficients", International Journal of Image and Graphics, 2019 (SCOPUS Indexed).
55. Sampurnananda Mishra, ChandraKantaSamal, **Navneet Yadav**, Rama Kanta Choudhury, "The Impact of Various Digitized Social Networking Media through Text, Images and Videos on Language Usage", International Journal of Scientific & Technology Research, 2019. (SCOPUS Indexed).
56. **Navneet Yadav**, R. K. Choudhury, "Connected Vehicles: Intelligent Transport Systems", Smart Sensors for Industrial Internet of Things. Internet of Things (Technology, Communications and Computing). Springer, Cham, 2019. https://doi.org/10.1007/978-3-030-52624-5_6
57. Harpal Singh, Pradeep Gaur, **Navneet Yadav**, Yogita Gupta, "SPARSITY BASED ADAPTIVE CHANNEL ESTIMATION BY BACTERIAL FORAGING OPTIMIZATION" Journal of Critical Reviews 7, 2020, 402-407. doi:10.31838/jcr.07.18.59
58. R.K. Choudhury, **Navneet Yadav**, Jaideep Kala, SonalikaBhandari, "Real Time Monitoring And Analysis of Troposphere Pollutants Using A Multipurpose Surveillance Drone" Internet of Drones: Opportunities and Challenges, Apple Academic Press, 2021
59. A. Bawa, R. K. Choudhury, C. K. Samal, **Navneet Yadav**, "Programmable Joint Computing Filter for Low-Power and High-Performance Applications" Advanced Computing. IACC 2020. Communications in Computer and Information Science, vol 1368. Springer, Singapore, 2021. https://doi.org/10.1007/978-981-16-0404-1_22
60. **Navneet Yadav**, H. Singh, "Introduction to Watermarking and Copyright Protection" Cyber Crime and Forensic Computing Modern Principles, Practices and Algorithms. De Gruyter, 2021
61. **Preeti Goyal**, Garima Srivastava, Sonam Rewari, and R. S. Gupta, "Controlling Ambipolarity and Rising Ion in TFETs for Enhanced Reliability: A Review", 5th IEEE International Conference on Recent Advances and Innovations in Engineering- ICRAIE 2020.
62. **Preeti Goyal**, Madan J, Srivastava G, Pandey R, Gupta RS. Performance Analysis of Drain Pocket Hetero Gate Dielectric DG-TFET: Solution for Ambipolar Conduction and Enhanced Drive Current. Silicon. 2022 Jan 7:1-1.
63. **Preeti Goya**l, Srivastava G,Madan J, Pandey R, Gupta RS. Source Material- Engineered Charge Plasma based Double Gate TFET for Analog/RF Applications In2021 IEEE International Conference on Industrial Electronic Research and Application (ICIERA) 2021 Dec 24 . IEEE.
64. **Anubha Goel**, Sonam Rewari, Seema Verma and R. S. Gupta, "Temperature-dependent gate-induced drain leakages assessment of dual-metal nanowire field-effect transistor—

analytical model" IEEE Transactions on Electron Devices, Volume: 66, Issue: 5 ,pp 2437 – 2445, May 2019. (Print ISSN: 0018-9383 Online ISSN: 1557-9646 Digital Object Identifier 10.1109/TED.2019.2898444, Impact Factor: 2.62).

65. **Anubha Goel**, Sonam Rewari, Seema Verma and R. S. Gupta, "Shallow Extension Engineered Dual Material Surrounding Gate (SEE-DM-SG) MOSFET for Improved Gate Leakages, Analysis of Circuit and Noise Performance," AEU-International Journal of Electronics and Communications (Elsevier), Volume: 111, pp 152924, Nov 2019. (Print ISSN: Online ISSN: 1434-8411 Digital Object Identifier: 10.1016/j.aeue.2019.152924, Impact Factor: 2.853).
66. **Anubha Goel**, Sonam Rewari, Seema Verma and R. S. Gupta, " High-K Spacer Dual-Metal Gate Stack UnderlapJunctionless Gate All Around (HK-DMGS-JGAA) MOSFET for High Frequency Applications, " Microsystem Technologies Journal (Springer), Volume: 26, Issue: 5, pp 1697-1705, Dec 2019. (Print ISSN: 0946-7076 Online ISSN: 1432-1858; Digital Object Identifier: 10.1007/s00542-019-04715-6, Impact Factor: 1.513).
67. **Anubha Goel**, Sonam Rewari, Seema Verma and R. S. Gupta, "Modeling of Shallow Extension Engineered-Dual Metal-Surrounding Gate (SEE-DM-SG) MOSFET- Gate Induced Drain Leakages (GIDL)," Indian Journal of Physics (Springer), Volume: 95, Issue: 2, pp 299-308, March 2020. (Online ISSN: 0946-7076, Digital Object Identifier: 10.1007/s12648-020-01704-8, Impact Factor: 1.242).
68. **Anubha Goel**, Sonam Rewari, Seema Verma and R. S. Gupta, "Physics based Analytic Modeling and Simulation of Gate-Induced-Drain-Leakage and Linearity Assessment in Dual-Metal Junctionless Accumulation Nano-Tube FET (DM-JAM-TFET)," Applied Physics A (Springer), Volume:126, pp. 1-4, May 2020. (Print ISSN: 0947-8396, Online ISSN: 1432-0630, Digital Object Identifier: 10.1007/s00339-020-03520-7, Impact Factor: 1.784).
69. **Anubha Goel**, Sonam Rewari, Seema Verma and R. S. Gupta, "Novel Dual-Metal Junctionless Nanotube FET (DMJN-TFET) for Improved Analog and Low Noise Applications," Journal of Electronic Materials (Springer), Volume: 50, Issue: 1, pp.: 108-119, Nov. 2020. (Print ISSN: 0361-5235, Digital Object Identifier: 10.1007/s11664-020-08541-9, Impact Factor: 1.774).
70. **Anubha Goel**, Sonam Rewari, Seema Verma and R. S. Gupta "Shallow Extension Engineered Dual Metal Surrounding Gate (SEE-DM-SG) MOSFET for lower leakages and Submillimeter Wave Applications", , 12th INDIACom; 2018 5th IEEE International Conference on Computing for Sustainable Global Development, BVICAM, 14-16 March, 2018, New Delhi, India.(ISSN: 0973-7529 ISBN: 978-93-80544-28-1)
71. **Anubha Goel**, Sonam Rewari, Seema Verma and R. S. Gupta, "Impact of Shallow Extension Engineering (SEE) on Analog and Digital Performance of Surrounding Gate (SG) MOSFET for High Frequency, High Gain and High Speed Real Time Applications", Micro2018; 5th International Conference on Microelectronics, Circuits & Systems, Applied Computer Technology, May, 19th - 20th , 2018, Bhubaneswar, Odisha, India.(ISBN: 81-85824-46-1)
72. **Anubha Goel**, Sonam Rewari, Seema Verma and R. S. Gupta "High-K Spacer Dual-Metal Gate Stack UnderlapJunctionless Gate All Around (HK-DMGSU-JGAA) MOSFET for High Frequency Applications", 8th International conference ; CCSN-2019

on “Computing, Communication and Sensor Networks”, 19-20 Oct, 2019 in Institute of Aeronautical Engineering, Hyderabad, Telengana, India. (ISBN:81-85824-5)

73. **Anubha Goel**, Sonam Rewari, Seema Verma and R. S. Gupta “Dual-Metal Junctionless Nanotube FET (DMJN-FET) for Improved Analog Applications”, AnubhaGoel, SonamRewari, SeemaVerma and R.S.Gupta, 8th International conference ; CCSN-2019 on “Computing, Communication and Sensor Networks”, 19-20 Oct, 2019 in Institute of Aeronautical Engineering, Hyderabad, Telengana, India.(ISBN: 81-85824-5)
74. **Anubha Goel**, Sonam Rewari, Seema Verma and R. S. Gupta, “GaN Based Dual-Metal Gate Stack Engineered Junctionless-Surrounding-Gate (DMSEJSG) MOSFET for High Power Applications”, 16th IEEE India Council International Conference; Indicon-2019 on “Applying Artificial Intelligence in Engineering for Prosperity and Betterment of Humanity”, 13-15 Dec, 2019 in Marwadi University, Rajkot, Gujarat, India. (ISBN: 978-1-7281-2327-1)
75. **Anubha Goel**, Sonam Rewari, Seema Verma and R. S. Gupta, “Dielectric Modulated Triple Metal Gate All Around MOSFET (TMGAA) for DNA Bio-Molecule Detection”, 2018 IEEE Electron Device Kolkata Conference (2018 IEEE EDKCON), The Pride Hotel, Kolkata, 24-25 November, 2018, Organized by IEEE EDS Kolkata Chapter, EDKCON 2018, Kolkata, India.(ISBN: 978-1-5386-6415-5)
76. Neeraj**, Anubha Goel**, Sharma S, Rewari S, Gupta RS. SiC-based analytical model for gate-stack dual metal nanowire FET with enhanced analog performance. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields.:e2986, Jan, 2022.
77. Swati Sharma, **Anubha Goel**, Sonam Rewari, Vandana Nath and R. S. Gupta “Enhanced Analog Performance and High-Frequency Applications of Dielectric Engineered High-K Schottky Nanowire FET,” Silicon-Springer.Jan 2022.
78. Swati Sharma, **Anubha Goel**, Sonam Rewari, Vandana Nath and R. S. Gupta .Gallium Nitride Cylindrical Schottky Barrier MOSFET(GaN-CSB-MOSFET) For High - Frequency Implementation. IEEE International Conference on Industrial Electronics Research and Application (ICIERA 2021), Dec, 2021.
79. **Umesh Chandra Singh**, Rajesh Mishra, Sudarshan Kumar, “Design and comparison of Substrate Integrated Waveguide Leaky Wave Antennas”, IOSR Journal of Electronics and Communication Engineering (IOSR-JECE) e-ISSN: 2278-2834,p- ISSN: 2278-8735.Volume 15, Issue 1, Ser. I (Jan-Feb 2020), PP 13-18.
80. Ekagra Gupta, Muskaan Kapahi, Sumanta Kumar Kundu, **Umesh Chandra Singh**, “SIW structured antenna for the application of RF and millimetre wave technology” presented in National conference on Next Generation Communication Technologies 21-22, November 2020 organised by IETE Bhopal, India.
81. **Umesh Chandra Singh**, Mishra R. Design and development of substrate Integrated waveguide based dual band antenna for planer phased radar application. IEEE International Conference on Industrial Electronic Research and Application (ICIERA) 2021 Dec 24 . IEEE.
82. **Umesh Chandra Singh**, Mishra R. Design of substrate integrated wave guide slot antenna for millimeter wave 5G application.IEEE International Conference on Industrial Electronic Research and Application (ICIERA) 2021 Dec 24 . IEEE

83. Rajni Sharma, M. Ravinder, **Nitin Sharma**, and Kanchan Sharma. "An optimal remote sensing image enhancement with weak detail preservation in wavelet domain." Journal of Ambient Intelligence and Humanized Computing (2021): 1-12.
84. **Nitin Sharma**, Rajni Sharma, and Kanchan Sharma. "Gray World Based Fuzzy C-Means Satellite Image Segmentation." In Energy Systems, Drives and Automations, pp. 193-204. Springer, Singapore, 2020.
85. **Sumanta Kumar Kundu**, and Pramod Kumar Singhal. "A slotted circularly polarized semicylindrical conformal patch antenna for EBS and BRS bands." Journal of Computational Electronics (2020): 1-12.
86. **Sumanta Kumar Kundu**, ShashankJaiswal, and Pramod Kumar Singhal. "Design of a novel circularly polarized patch antenna on Elliptical structure." In Smart Systems and IoT: Innovations in Computing, pp. 835-844. Springer, Singapore, 2020.
87. **Sumanta Kumar Kundu**, and P. K. Singhal. "A Novel Circularly Polarized Conformal Patch Antenna using Asymmetrically Slotted Ground." (2019).
88. **Sumanta Kumar Kundu**, and Pramod Kumar Singhal. "Design of slit loaded Planar and Curved Patch Antenna." structure 8: 9.
89. **Sumanta Kumar Kundu**, ShashankJaiswal, and Pramod Kumar Singhal. "Study and Comparison of Planar and Cylindrical Patch Antenna." In 2018 8th International Conference on Communication Systems and Network Technologies (CSNT), pp. 10-16. IEEE, 2018.
90. **Sumanta Kumar Kundu,** Damanpreet Singh Walia, Shashank Jaiswal, and P. K. Singhal. "High-Gain L Probe-Fed Planar and Cylindrical Patch Antenna for X Band Applications." In Engineering Vibration, Communication and Information Processing, pp. 205-217. Springer, Singapore, 2019.
91. **Sumanta Kumar Kundu** and Shashank Jaiswal "Asymmetrically Slotted, Tunable And Single Mode Circularly Polarised Microstrip Antenna For Iot Application" Procedia Computer Science 173 (2020): 86-93.
92. Kartik Mendiratta, Manav Gupta, **Sumanta Kumar Kundu**, P K Singhal, "Attainment of Circular Polarization in Planar Patch & Conformal Patch Antenna using Ring Slotted Ground Plane" International Journal of Advanced Science and Technology, 29(3), 425 – 439, 2020.
93. Ekagra Gupta, Muskaan Kapahi, **Sumanta Kumar Kundu**, Umesh Chandra Singh, "SIW structured antenna for the application of RF and millimetre wave technology" presented in National conference on Next Generation Communication Technologies 21-22, November 2020 organised by IETE Bhopal India.
94. Tarun, Kunal, **Sumanta Kumar Kundu**, P K Singhal "A CPW-fed cup shaped UWB antenna for the application of next generation communication system" National conference on Next Generation Communication Technologies 21-22, November 2020 organised by IETE Bhopal, India
95. Deepanshu, Ayush, **Sumanta Kumar Kundu**, P K Singhal, "Circularly polarized patch antenna for IoT application," National conference ori Next Generation Communication Technologies 21-22,November 2020 organised by IETE Bhopal, India.
96. Muskaan Kapahi, Ekagra Gupta, **Sumanta Kumar Kundu**, Sanyog Rawat, "Design of circularly polarized irregular octogonal shaped and dumbbell slotted planar and conformal patch antenna" Scientia Iranica: SCI (sci search)-

DOI:10.24200/SCI.2021.57153.5379. Articles in Press: Accepted Manuscript Available Online from 06 December 2021

97. Vikash Tiwari, Varun Tanwar, Kumar Ankit, **Sumanta Kumar Kundu**, SanyogRawat,"Circularly polarized planar and nonplanar microstrip patch antenna for S band application" submitted in international conference ICIERA-2021, 22-24 December 2021 in, MAIT New Delhi –accepted and presented for inclusion in IEEE explore
98. **Swati Sharma**,Sonam Rewari, Vandana Nath,S.S. Deswal,R. S. Gupta, "Schottky Barrier Double Surrounding Gate MOSFET For High-Frequency Implementation" 5th IEEE International Conference on Recent Advances and Innovations in Engineering-ICRAIE 2020 December1-3.
99. **Swati Sharma**, Anubha Goel, Sonam Rewari, Vandana Nath and R. S. Gupta "Enhanced Analog Performance and High-Frequency Applications of Dielectric Engineered High-K Schottky Nanowire FET," Silicon-Springer.Jan 2022.
100. **Swati Sharma**, Anubha Goel, Sonam Rewari, Vandana Nath and R. S. Gupta .Gallium Nitride Cylindrical Schottky Barrier MOSFET(GaN-CSB-MOSFET) For High - Frequency Implementation. IEEE International Conference on Industrial Electronics Research and Application (ICIERA 2021), Dec, 2021.
101. **Rajni Yadav**, C.S. Rai, "Linear phase sparse system identification in the presence of impulsive noise", International Journal of Electronics Letters, Taylor and Francis vol.7, no. 3, pp. 321-337, 2019.
102. **Rajni Yadav**, C. S. Rai, "Composite Norm Proportionate Normalized Minimum Error Entropy Algorithm for Clump Sparse Channel Estimation", International Journal of Advanced Research in Engineering and Technology (IJARET), vol. 11, no. 7, pp. 543-555, July, 2020
103. **Rajni Yadav**, C. S. Rai, "l1-Norm Penalized Bias Compensated Linear Constrained Affine Projection Algorithm", International Journal of Engineering and Advanced Technology (IJEAT), vol. 9, no. 3, pp. 1809-1816, February 2020.
104. **Rajni Yadav**, C. S. Rai, KanikaAggarwal, "l1-Norm Constrained Minimum Error Entropy Algorithm", International Journal of Engineering and Advanced Technology (IJEAT), vol. 9, no. 3, pp. 2350-2354, February 2020.
105. **Rajni Yadav**, C. S. Rai, "Bias Compensated Linear Constrained Affine Projection Sign Algorithm, Journal of Innovation in Electronics and Communication Engineering, vol. 9, no. 2, 2019
106. **Rajni Yadav**, C.S. Rai, "Constrained Sparse Channel Estimation in the Presence of Impulsive Measurement Noise against Noisy Input", Circuit System and Signal Processing (CSSP), Springer (Revision)
107. **Rajni Yadav**, C.S. Rai, "Constrained System identification in the Presence of Impulsive Channel Noise against Noisy Input", Signal, Image and Video Processing (SIVP), Elsevier (Revision)
108. **Rajni Yadav**, C.S. Rai, "A Novel p-Norm-like-Set Membership Affine Projection Algorithm in Sparse System Identification", In the proceedings of Applications of Artificial Intelligence Techniques in Engineering, Advances in Intelligent Systems and Computing, Springer, Singapore, vol. 697, 2019.
109. **Rajni Yadav**, Sonia Jain, C.S. Rai, "Correntropy-Induced Metric-Based Variable Step Size Normalized Least Mean Square Algorithm in Sparse System Identification", In the proceedings of First International Conference on Sustainable Technologies for

Computational Intelligence. Advances in Intelligent Systems and Computing, Springer, Singapore, vol. 1045, 2020.

110. **Rajni Yadav**, C. S. Rai and Sonia Jain, "Non–Uniform Norm Based Minimum Error Entropy Algorithm in Sparse System Identification," 2019 International Conference on Computing, Power and Communication Technologies (GUCON), NCR New Delhi, India, pp. 622-627, 2019.
111. **Rajni Yadav**, C. S. Rai, "Performance Analysis of ℓ0-Norm Constraint Variable Step Size Normalized Least Mean Square Algorithm," in proceedings of IEEE International Conference on Recent Innovations in Electrical, Electronics & Communication Engineering (ICRIEECE), Bhubaneswar, India, 2018, pp. 1450-1455, 2018.
112. **Rajni Yadav**, C.S. Rai, "Sparse System Identification in the Presence of Noisy Input Signal using Biased Compensator Minimum Error Entropy Algorithm", WSEAS Transactions on Signal Processing, vol. 15, 2019.
113. **Rajni Yadav**, C.S. Rai, "PNLMS-Based Computational Cost Efficient Adaptive Algorithms for Sparse Channel Identification", International Conference on Electrical, Electronics, Computers, Communication, Mechanical and Computing (EECCMC), IEEE, Jan. 2018, Tamilnadu.
114. **Rajni**, C.S. Rai, "Constrained System identification in the Presence of Impulsive Channel Noise against Noisy Input", Signal, Image and Video Processing (SIVP), Elsevier ,October, 2021, doi: 10.1007/s11760-021-01956.
115. Rajni Yadav, C. S. Rai and **Sonia Jain**, "Non–Uniform Norm Based Minimum Error Entropy Algorithm in Sparse System Identification," 2019 International Conference on Computing, Power and Communication Technologies (GUCON), NCR New Delhi, India, pp. 622-627, 2019.
116. Rajni Yadav, **Sonia Jain**, C.S. Rai, "Correntropy-Induced Metric-Based Variable Step Size Normalized Least Mean Square Algorithm in Sparse System Identification", In the proceedings of First International Conference on Sustainable Technologies for Computational Intelligence. Advances in Intelligent Systems and Computing, Springer, Singapore, vol. 1045, 2020.
117. Anubha Goel, **Sonam Rewari**, Seema Verma and R.S. Gupta, "Temperature-dependent gate-induced drain leakages assessment of dual-metal nanowire field-effect transistor—analytical model" IEEE Transactions on Electron Devices, Volume: 66, Issue: 5 ,pp 2437 – 2445, May 2019. (Print ISSN: 0018-9383 Online ISSN: 1557-9646 Digital Object Identifier 10.1109/TED.2019.2898444, Impact Factor: 2.62).
118. Anubha Goel, **Sonam Rewari**, Seema Verma and R.S. Gupta, "Shallow Extension Engineered Dual Material Surrounding Gate (SEE-DM-SG) MOSFET for Improved Gate Leakages, Analysis of Circuit and Noise Performance," AEU-International Journal of Electronics and Communications (Elsevier), Volume: 111, pp 152924, Nov 2019. (Print ISSN: Online ISSN: 1434-8411 Digital Object Identifier: 10.1016/j.aeue.2019.152924, Impact Factor: 2.853).
119. Anubha Goel, **Sonam Rewari**, Seema Verma and R.S. Gupta, " High-K Spacer Dual-Metal Gate Stack UnderlapJunctionless Gate All Around (HK-DMGS-JGAA) MOSFET for High Frequency Applications, " Microsystem Technologies Journal (Springer), Volume: 26, Issue: 5, pp 1697-1705, Dec 2019. (Print ISSN: 0946-7076 Online ISSN: 1432-1858; Digital Object Identifier: 10.1007/s00542-019-04715-6, Impact Factor: 1.513).

120. Anubha Goel, **Sonam Rewari**, Seema Verma and R.S. Gupta, "Modeling of Shallow Extension Engineered-Dual Metal-Surrounding Gate (SEE-DM-SG) MOSFET- Gate Induced Drain Leakages (GIDL)," Indian Journal of Physics (Springer), Volume: 95, Issue: 2, pp 299-308, March 2020. (Online ISSN: 0946-7076, Digital Object Identifier: 10.1007/s12648-020-01704-8, Impact Factor: 1.242).
121. Anubha Goel, **Sonam Rewari**, Seema Verma and R.S. Gupta, "Physics based Analytic Modeling and Simulation of Gate-Induced-Drain-Leakage and Linearity Assessment in Dual-Metal Junctionless Accumulation Nano-Tube FET (DM-JAM-TFET)," Applied Physics A (Springer), Volume:126, pp. 1-4, May 2020. (Print ISSN: 0947-8396, Online ISSN: 1432-0630, Digital Object Identifier: 10.1007/s00339-020-03520-7, Impact Factor: 1.784).
122. Anubha Goel, **Sonam Rewari**, Seema Verma and R.S. Gupta, "Novel Dual-Metal Junctionless Nanotube FET (DMJN-TFET) for Improved Analog and Low Noise Applications," Journal of Electronic Materials (Springer), Volume: 50, Issue: 1, pp.: 108-119, Nov. 2020. (Print ISSN: 0361-5235, Digital Object Identifier: 10.1007/s11664-020-08541-9, Impact Factor: 1.774).
123. Anubha Goel, **Sonam Rewari**, Seema Verma and R.S. Gupta, "Shallow Extension Engineered Dual Metal Surrounding Gate (SEE-DM-SG) MOSFET for lower leakages and Submillimeter Wave Applications", , 12th INDIACom; 2018 5th IEEE International Conference on Computing for Sustainable Global Development, BVICAM, 14-16 March, 2018, New Delhi, India.(ISSN: 0973-7529 ISBN: 978-93-80544-28-1)
124. Anubha Goel, **Sonam Rewari**, Seema Verma and R.S. Gupta, "Impact of Shallow Extension Engineering (SEE) on Analog and Digital Performance of Surrounding Gate (SG) MOSFET for High Frequency, High Gain and High Speed Real Time Applications", Micro2018; 5th International Conference on Microelectronics, Circuits & Systems, Applied Computer Technology, May, 19th - 20th , 2018, Bhubaneswar, Odisha, India.(ISBN: 81-85824-46-1)
125. Anubha Goel, **Sonam Rewari**, Seema Verma and R.S. Gupta, "Dielectric Modulated Triple Metal Gate All Around MOSFET (TMGAA) for DNA Bio-Molecule Detection", 2018 IEEE Electron Device Kolkata Conference (2018 IEEE EDKCON), The Pride Hotel, Kolkata, 24-25 November, 2018, Organized by IEEE EDS Kolkata Chapter, EDKCON 2018, Kolkata, India.(ISBN: 978-1-5386-6415-5)
126. Anubha Goel, **Sonam Rewari**, Seema Verma and R.S. Gupta, "High-K Spacer Dual-Metal Gate Stack UnderlapJunctionless Gate All Around (HK-DMGSU-JGAA) MOSFET for High Frequency Applications", 8th International conference ; CCSN-2019 on "Computing, Communication and Sensor Networks", 19-20 Oct, 2019 in Institute of Aeronautical Engineering, Hyderabad, Telengana, India. (ISBN:81-85824-5)
127. Anubha Goel, **Sonam Rewari**, Seema Verma and R.S. Gupta, "Dual-Metal Junctionless Nanotube FET (DMJN-FET) for Improved Analog Applications", AnubhaGoel, SonamRewari, SeemaVerma and R.S.Gupta, 8th International conference ; CCSN-2019 on "Computing, Communication and Sensor Networks", 19-20 Oct, 2019 in Institute of Aeronautical Engineering, Hyderabad, Telengana, India.(ISBN: 81-85824-5)
128. Anubha Goel, **Sonam Rewari**, Seema Verma and R.S. Gupta, "GaN Based Dual-Metal Gate Stack Engineered Junctionless-Surrounding-Gate (DMSEJSG) MOSFET for High Power Applications", 16th IEEE India Council International Conference; Indicon-2019 on "Applying Artificial Intelligence in Engineering for Prosperity and Betterment of

Humanity", 13-15 Dec, 2019 in Marwadi University, Rajkot, Gujarat, India. (ISBN: 978-1-7281-2327-1)

129. Swati Sharma,**Sonam Rewari**, Vandana Nath, S.S. Deswal, R. S. Gupta, "Schottky Barrier Double Surrounding Gate MOSFET For High-Frequency Implementation" 5th IEEE International Conference on Recent Advances and Innovations in Engineering-ICRAIE 2020 December1-3.
130. Rajni Yadav, C. S. Rai, **Kanika Aggarwal,** "l1-Norm Constrained Minimum Error Entropy Algorithm", International Journal of Engineering and Advanced Technology (IJEAT), vol. 9, no. 3, pp. 2350-2354, February 2020.
131. **Kanika Aggarwal**, Rai CS, Yadav Rajni. Performance of Constrained Affine Projection Sign Algorithm in Adaptive Beamforming. ICIERA. 2021 Dec.
132. **Sumedha Gupta**, NeetaPandey and R. S. Gupta, "Investigation of Dual-Material Double Gate Junctionless Accumulation-Mode Cylindrical Gate All Around (DMDG-JLAM-CGAA) MOSFET with High-k Gate Stack for low Power Digital Applications" *2020 IEEE 17th India Council International Conference (INDICON)*, New Delhi, India, 2020, pp. 1-4, doi: 10.1109/INDICON49873.2020.9342380.
133. **Sumedha Gupta**, Pandey N, Gupta RS. Analog Performance of Dual-Metal Gate Stack Architecture of Junctionless Accumulation-Mode Cylindrical Surrounding Gate (DMGSA-JAM-CSG) MOSFET. In2021 Devices for Integrated Circuit (DevIC) 2021 May 19 (pp. 1-5). IEEE.
134. **Sumedha Gupta**, Pandey N, Gupta RS. Analytical modeling of dual-metal gate stack engineered junctionless accumulation-mode cylindrical surrounding gate (DMGSE-JAM-CSG) MOSFET. Applied Physics A. 2021 July;127(7):1-0.
135. **Sumedha Gupta**, Pandey N, Gupta RS. Temperature dependency and linearity assessment of dual-metal gate stack junctionless accumulation-mode cylindrical surrounding gate (DMGS-JAM-CSG) MOSFET. PhysicaScripta. 2021 Nov 8;96(12):124055.
136. **Vaibhav Nijhawan**, Davinder Miglani, Karan Arora, Shivam Gupta "Design of Low power High Speed Current Comparator, based Flash ADC" 12th INDIACom-2018, IEEE conference ID; 42835, 14th - 16th March, 2018
137. Dhruv Aggarwal, Aditya Rastogi, Parv Rustagi, **Vaibhav Nijhawan**, "Real Time RF Based Gesture Controlled Robotic Vehicle" 15th INDIACom-2021, IEEE conference ID; 51348, 17th - 19th March, 2021
138. Deepanshu Tanwar, **Vaibhav Nijhawan**, Pragya Sinha, Rashi Gupta "Design of Low cost Women Safety System using GPS and GSM" 15th INDIACom-2021, IEEE conference ID; 51348, 17th - 19th March, 2021
139. Vashist A, Kansal R, **Vaibhav Nijhawan**, Zafar Z. A Fingerprint Based Ignition System in Vehicles. International Journal of Scientific & Engineering Research. June 2021.
140. **Neeraj Malik**, Shalu Garg and Kanika Singhal, "Demand Side Management Using GUI," 2019 3rd International Conference on Recent Developments in Control, Automation & Power Engineering (RDCAPE), Noida, India, 2019, pp. 100-105, doi: 10.1109/RDCAPE47089.2019.8979117.

141. Rohan and **Neeraj**, "Behavior of Cascaded Binary Symmetric Channel for different number of cascades," 2020 Fourth International Conference on I-SMAC (IoT in Social, Mobile, Analytics and Cloud) (I-SMAC), Palladam, India, 2020, pp. 1159-1164, doi: 10.1109/I-SMAC49090.2020.9243336.
142. **Neeraj**, Sharma S, Goel A, Rewari S, Gupta RS. Gate-Stack Dual Metal (DM) Nanowire FET with Enhanced Analog Performance for High Frequency Applications. In 2021 Devices for Integrated Circuit (DevIC) 2021 May 19 (pp. 373-377). IEEE.
143. **Neeraj,** Goel A, Sharma S, Rewari S, Gupta RS. SiC-based analytical model for gate-stack dual metal nanowire FET with enhanced analog performance. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields.:e2986, Jan, 2022.
144. Kanika Singhal, and **Shalu Garg**. "Technology Based Intervention to Improve Social Skills in Students on Autism Spectrum" 2019 3rd International Conference on Recent Developments in Control, Automation & Power Engineering (RDCAPE), IEEE, 2019.
145. Neeraj Malik, **Shalu Garg** and Kanika Singhal, "Demand Side Management Using GUI," 2019 3rd International Conference on Recent Developments in Control, Automation & Power Engineering (RDCAPE), Noida, India, 2019, pp. 100-105, doi: 10.1109/RDCAPE47089.2019.8979117.
146. Dipika Sharma, **Sudarshan Kumar**, Sonu Kumar, "Design and Simulation of Substrate Integrated waveguide and Substrate Integrated Waveguide Antennas", IOSR Journal of Electronics and Communication Engineering (IOSR-JECE) e-ISSN: 2278-2834,p- ISSN: 2278-8735.Volume 15, Issue 2, Ser. I (Mar-Apr 2020), PP 18-24.
147. Umesh Chandra Singh, Rajesh Mishra, **Sudarshan Kumar**, "Design and comparison of Substrate Integrated Waveguide Leaky Wave Antennas", IOSR Journal of Electronics and Communication Engineering (IOSR-JECE) e-ISSN: 2278-2834,p- ISSN: 2278-8735.Volume 15, Issue 1, Ser. I (Jan-Feb 2020), PP 13-18.
148. **Anamika Jain**, Neeta Pandey, "New algorithm for the DHT and it'sverilog Implementation" Journal of Innovative Technology and Exploring Engineering (IJITEE), volume-9, issue-4,page no 2908-2912, February 2020, ISSN:2278-3075
149. **Anamika Jain**, Neeta Pandey, "Algorithm for computation of DCT and its Implementation using a systolic architecture", International Journal of Engineering and Advanced Technology (IJEAT), vol-9, Issue-4, Page no. 2162-2167, April 2020, ISSN:2249-8958
150. **Anamika Jain**, Neeta Pandey, "Implementation of DST realization through systolic architecture" International conference on Microelectronics computing and communication systems, MCCS-2020, IETE and ISVE, Ranchi
151. **Rohit Rana**, Vijyant Agarwal, Prerna Gaur, and Harish Parthasarathy. "Design of Optimal UKF State Observer-Controller for Stochastic Dynamical Systems." IEEE Transactions on Industry Applications (2020).
152. **Rohit Rana**, Prerna Gaur, Vijyant Agarwal, and Harish Parthasarathy. "Estimation of robot states with poisson process based on EKF approximate of Kushner filter: a completely coordinate free Lie group approach." Meccanica (2021): 1-23.
153. **Rohit Rana,** Prerna Gaur, Vijyant Agarwal, and Harish Parthasarathy Parameter estimation, data compression and stochastic noise elimination in robotics: a wavelet domain-based integrated approach. Nonlinear Dynamics. 2022 Jan 28:1-23.
154. **Rohit Rana,** Prerna Gaur, Vijyant Agarwal, and Harish Parthasarathy. Analysis of exit probability for a trajectory tracking robot in case of a rare event. Robotica. 2021:1-26.

155. **Ajay K Gupta,** Ghosh S, Bhatnagar MR. Pricing Scheme for UAV-Enabled Charging of Sensor Network. In2021 IEEE 18th India Council International Conference (INDICON) 2021 Dec 19 (pp. 1-6). IEEE.